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CORRECTED PUBLICATION(54) **MASK GROUP AND METHOD FOR
FABRICATING AN ORGANIC
LUMINESCENCE LAYER, DISPLAY PANEL
AND DRIVING METHOD**(30) **Foreign Application Priority Data**

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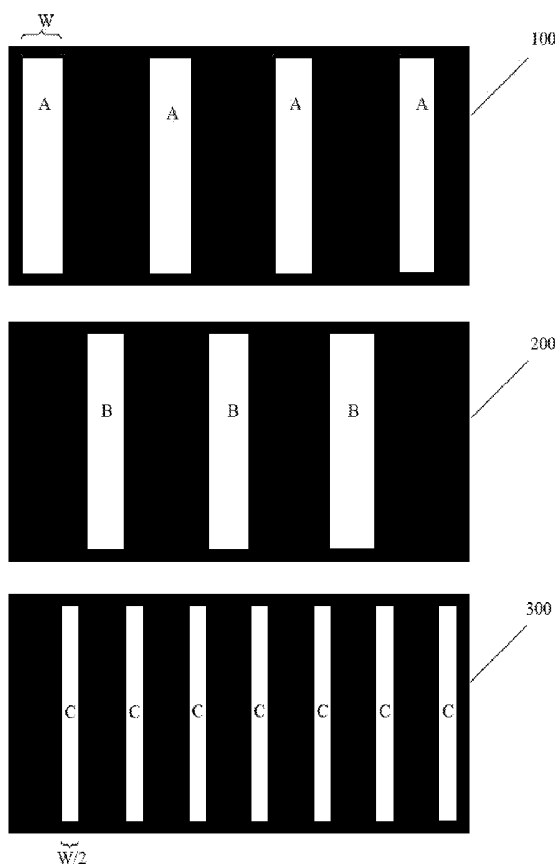
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Beijing (CN)(57) **ABSTRACT**(21) Appl. No.: **15/129,626**(22) PCT Filed: **Jan. 14, 2016**(86) PCT No.: **PCT/CN2016/070873**

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See Claims 1 and 17.

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The disclosure provides a mask plate assembly, a method for fabricating an electroluminescent layer, a display panel and a method for driving the same. The mask plate assembly includes a first color mask plate, a second color mask plate, and a third color mask plate each having a plurality of rectangular openings. First openings of the first color mask plate and second openings of the second color mask plate are of a length in a row direction, which is substantially twice as that of third openings of the third color mask plate in the row direction. When the respective mask plates are aligned with one another, the first openings, the second openings, and the third openings are arranged alternately in an order of a first opening, a third opening, a second opening, and another third opening.



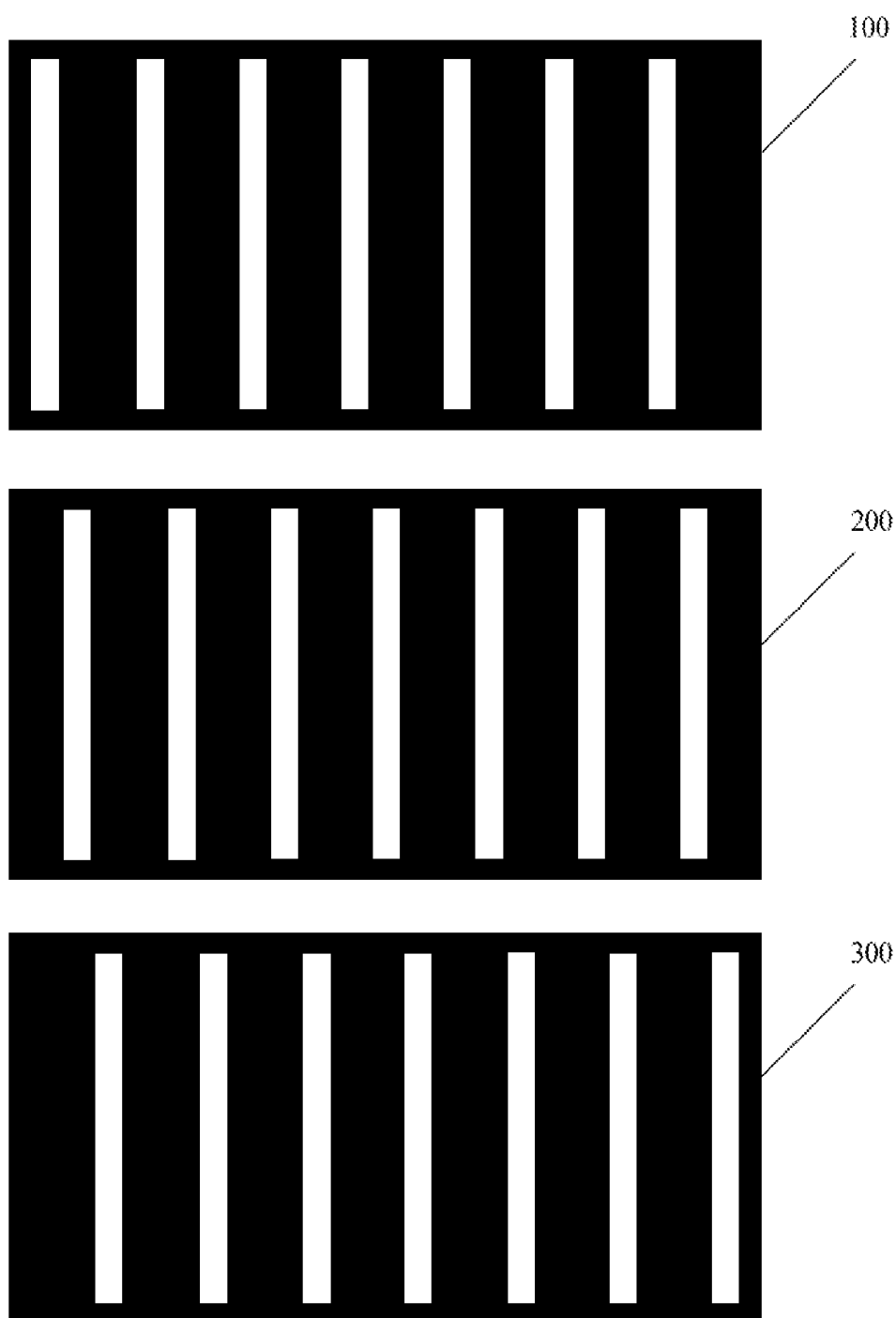


FIG. 1 (PRIOR ART)

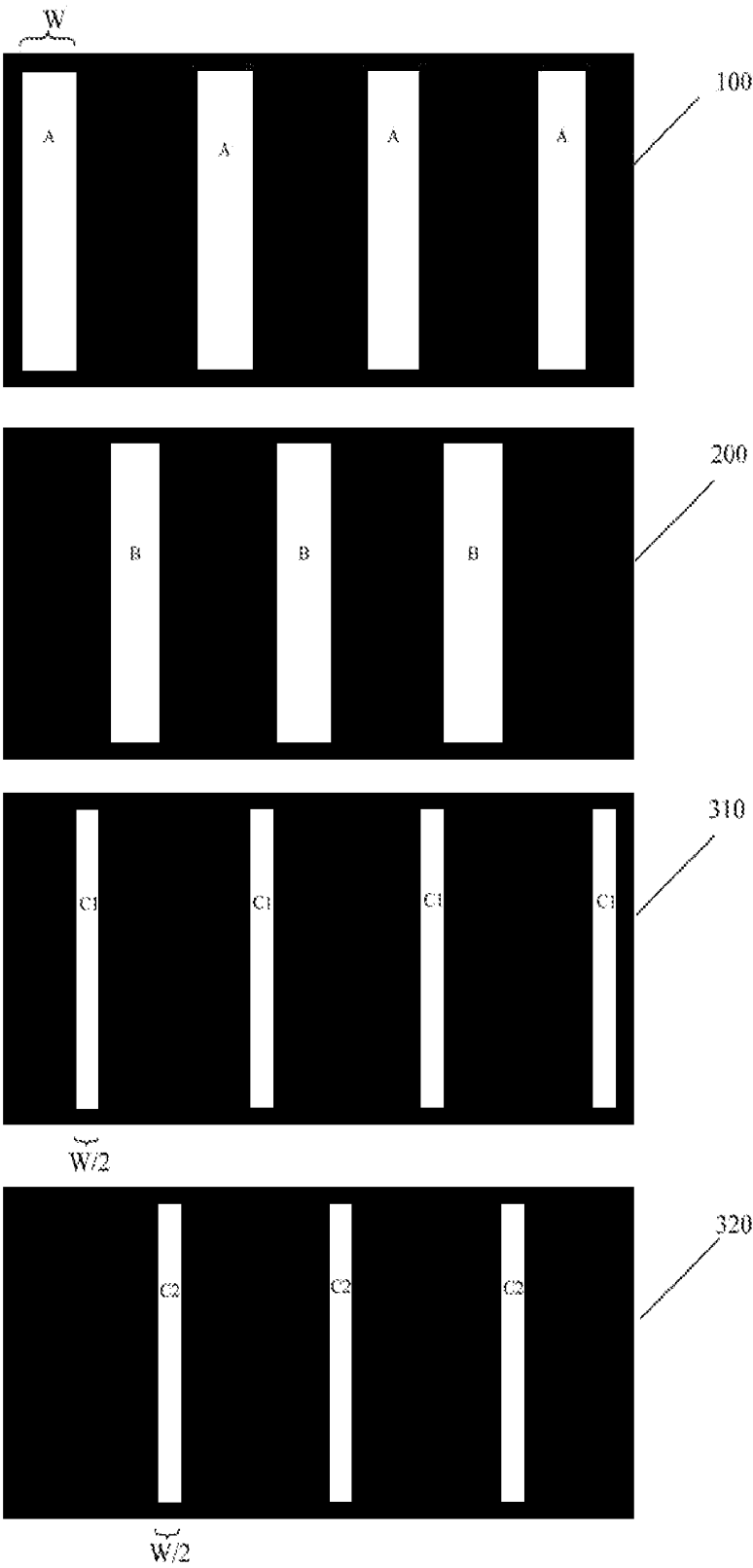


FIG. 2

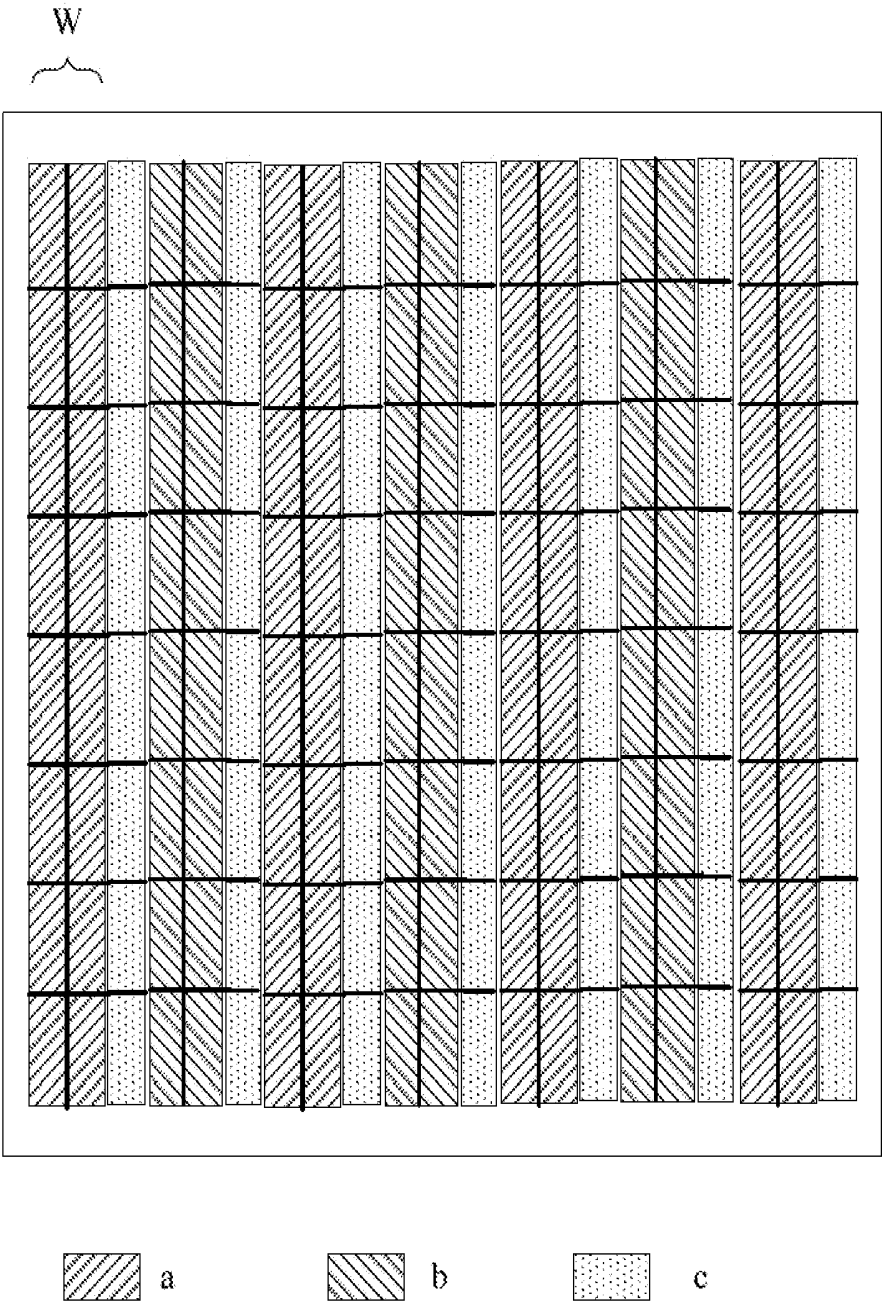


FIG. 3

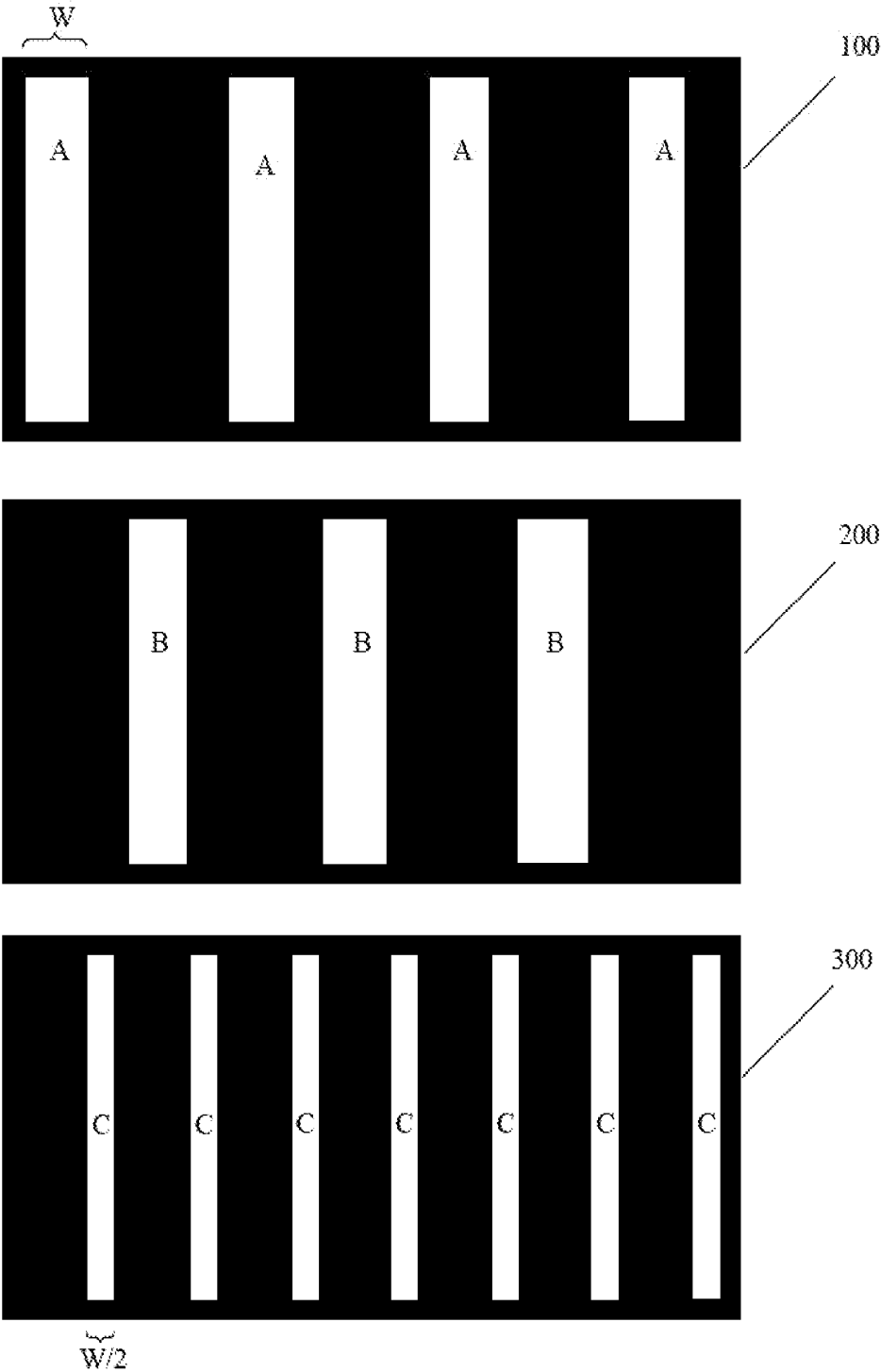


FIG. 4

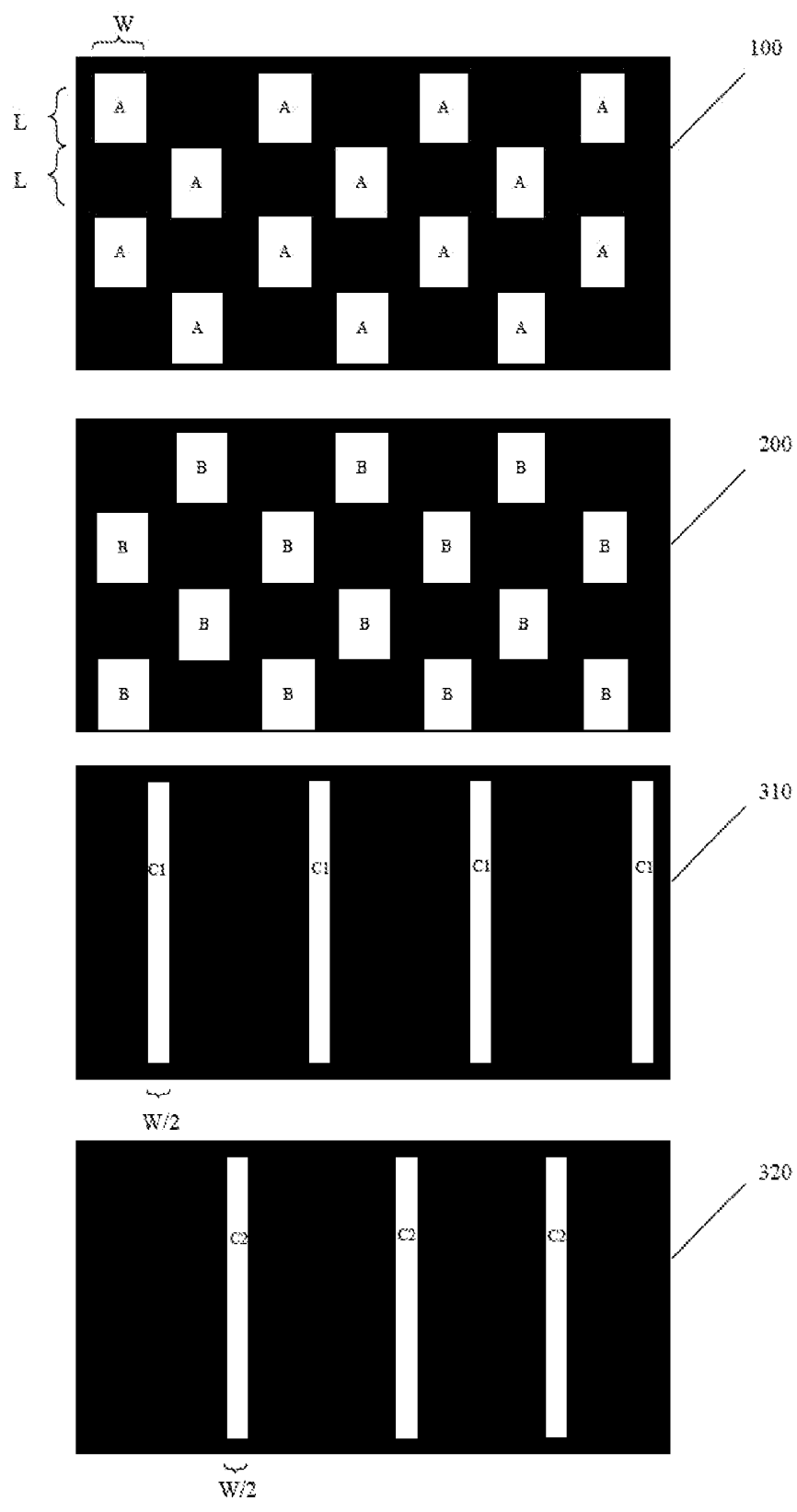


FIG. 5

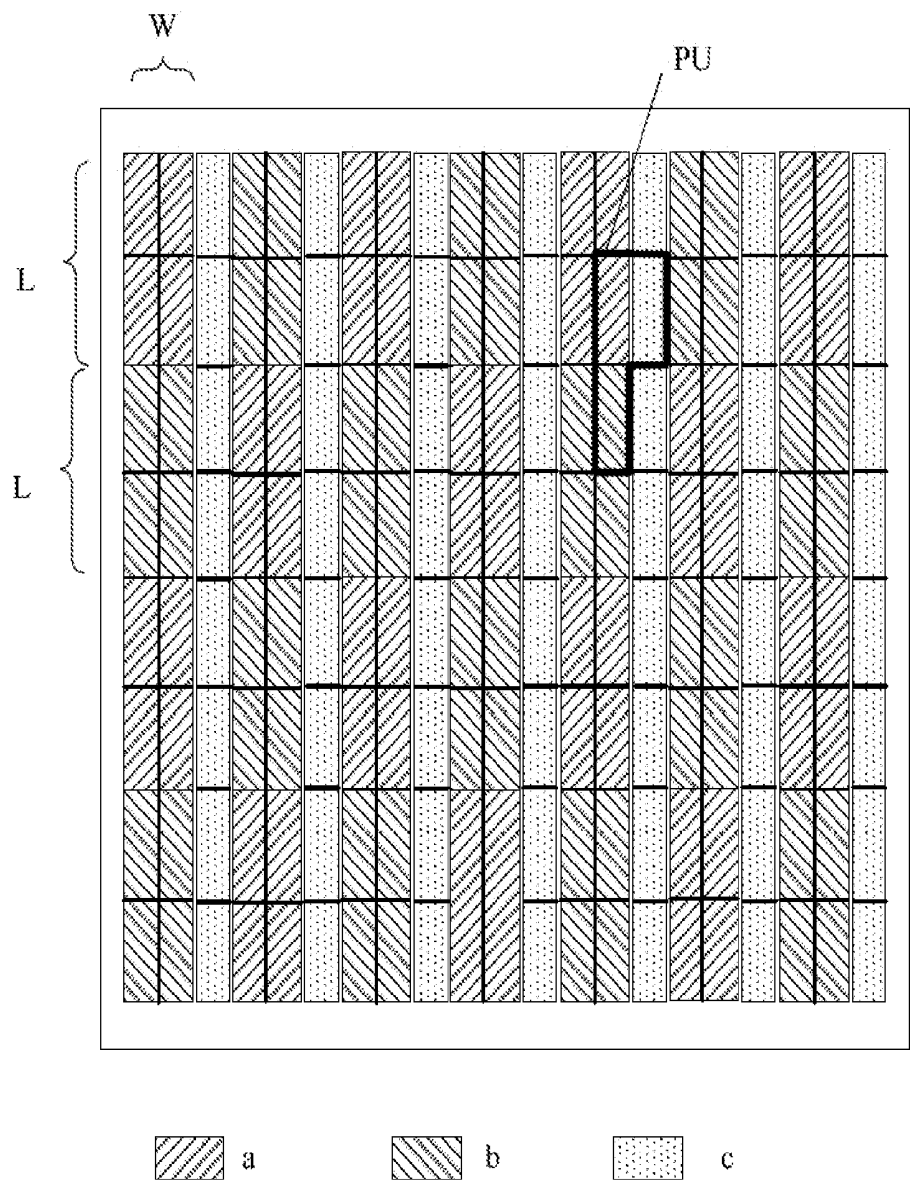


FIG. 6

**MASK GROUP AND METHOD FOR
FABRICATING AN ORGANIC
LUMINESCENCE LAYER, DISPLAY PANEL
AND DRIVING METHOD**

**CROSS REFERENCE TO RELATED
APPLICATIONS**

[0001] This application is a National Stage entry of PCT/CN2016/070873 filed Jan. 14, 2016, which claims priority of Chinese Patent Application No. 201510446792.X, which was filed on Jul. 27, 2015, the disclosures of which are incorporated herein by reference in their entirety.

BACKGROUND

[0002] This disclosure relates to the field of display technology, in particular to a mask plate assembly, a method for fabricating an electroluminescent layer, a display panel and a method for driving the same.

[0003] An Organic Light-Emitting Diode (OLED) display panel basically includes an anode, a light-emitting layer formed by organic electroluminescent materials, and a cathode.

[0004] There are mainly two methods for achieving color displaying of OLED, one of which is to achieve color displaying by using a combination of a white-light organic electroluminescent material and a color filter. However, this method degrades displaying brightness for color displaying, due to the fact that a part of light rays are absorbed by the color filter.

[0005] Another method is to form sub-pixels for respective colors by evaporating electroluminescent materials for different primary colors using high precision mask plates. FIG. 1 is a diagram illustrating a conventional mask plate assembly in related arts. As shown in FIG. 1, the mask plate assembly includes three mask plates **100**, **200**, and **300**, each of which is used to form sub-pixels for displaying a corresponding color. Each of mask plates is provided with a plurality of elongated openings, each of which is used to form one column of sub-pixels. In addition, positions for forming openings of respective mask plates are complementary, thereby forming a pixel array having sub-pixels for displaying three colors distributed therein.

[0006] Displaying resolution depends on areas of sub-pixels formed by an evaporation process using mask plates. Namely, the higher densely sub-pixels are distributed, the smaller their areas are, which requires that widths of respective openings included in the mask plates should be smaller. Accordingly, within respective mask plates, a width of grids between respective openings (which is twice as that of openings, as shown in FIG. 1) need to be smaller, which may easily result in deform of grids and adverse effect on shapes of sub-pixels to be fabricated.

BRIEF DESCRIPTION

[0007] In a first aspect, embodiments of the present disclosure provide a mask plate assembly, which includes a first mask plate having a plurality of first openings, a second mask plate having a plurality of second openings, and a third mask plate having a plurality of third openings. The first openings and the second openings are of a same length in a row direction, which is substantially twice as that of the third openings in the row direction. When the first mask plate, the second mask plate, and the third mask plate are aligned with

one another, the first openings, the second openings, and the third openings are arranged alternately in an order of a first opening, a third opening, a second opening, and another third opening.

[0008] In one possible embodiment, the first openings and the second openings are of a same length in a column direction, and the first openings and the second openings are aligned with one another in the column direction and arranged alternately.

[0009] In one possible embodiment, the third mask plate includes a first sub-mask plate having a plurality of the third openings and a second sub-mask plate having a plurality of the third openings. When the first sub-mask plate and the second sub-mask plate are aligned with one another, the third openings of the first sub-mask plate and the third openings of the second sub-mask plate are arranged alternately in the row direction.

[0010] In one possible embodiment, the first openings, the second openings, and the third openings are all of a rectangular shape, and the first mask plate, and/or the second mask plate, and/or the third mask plate is(are) a metal mask plate.

[0011] In a second aspect, embodiments of the present disclosure provide a method for fabricating a display panel component by using any one of the above mask plate assemblies. The method includes fabricating a first layer by using the first mask plate, fabricating a second layer by using the second mask plate, and fabricating a third layer by using the third mask plate.

[0012] In one possible embodiment, the first layer is a first electroluminescent layer for displaying a first color, the second layer is a second electroluminescent layer for displaying a second color, and the third layer is a third electroluminescent layer for displaying a third color.

[0013] In one possible embodiment, each of the first electroluminescent layer, the second electroluminescent layer, and the third electroluminescent layer includes a plurality of electroluminescent material blocks for displaying respective colors. Each of the plurality of electroluminescent material blocks corresponds to one corresponding opening of a certain mask plate for fabricating a corresponding electroluminescent layer.

[0014] In one possible embodiment, fabricating respective electroluminescent layer patterns by using respective mask plates includes fabricating respective electroluminescent layer patterns by an evaporation process.

[0015] In a third aspect, embodiments of the present disclosure provide a method for fabricating a display panel including steps of fabricating respective electroluminescent layers by using any one of the above methods for fabricating the same.

[0016] In one possible embodiment, the method further includes a step of fabricating a pixel electrode that includes a plurality of pixel electrode blocks. Each of electroluminescent material blocks included in the first electroluminescent layer and the second electroluminescent layer corresponds to two columns of pixel electrode block, and each of electroluminescent material blocks included in the third electroluminescent layer corresponds to one column of pixel electrode block.

[0017] In one possible embodiment, within the mask plate assembly used by the method for fabricating the display panel component, the first openings and the second openings are aligned with one another in the column direction and arranged alternately. And each of electroluminescent mate-

rial blocks included in the first electroluminescent layer pattern and the second electroluminescent layer pattern corresponds to two rows and two columns of pixel electrode block.

[0018] In a fourth aspect, embodiments of the present disclosure provide an Organic Light-Emitting Diode (OLED) display panel including a sub-pixel array having three-color sub-pixels. Each row of the sub-pixel array includes the three-color sub-pixels arranged alternately in an order of a first color sub-pixel, another first color sub-pixel, a third color sub-pixel, a second color sub-pixel, another second color sub-pixel, and another third color sub-pixel.

[0019] In one possible embodiment, each column of the sub-pixel array includes the three-color sub-pixels arranged alternately in an order of a first color sub-pixel, another first color sub-pixel, a second color sub-pixel, and another second color sub-pixel.

[0020] In a fifth aspect, embodiments of the present disclosure provide a method for driving the OLED display panel which is applied to any one of the above display panels. The method includes dividing respective sub-pixels within the sub-pixel array into a plurality of pixel units for display driving.

[0021] In one possible embodiment, the plurality of pixel units includes a first color sub-pixel, a second color sub-pixel immediately adjacent to the first color sub-pixel in a column direction, and a third color sub-pixel immediately adjacent to the first color sub-pixel or the second color sub-pixel in a row direction.

[0022] In one possible embodiment, the plurality of pixel units includes a first color sub-pixel, a second color sub-pixel, and a third color sub-pixel arranged between the first color sub-pixel and the second color sub-pixel.

[0023] In a sixth aspect, embodiments of the present disclosure provide a display device including any one of the above display panels.

[0024] According to the mask plate assembly provided by the disclosure, in comparison between the first mask plate and the second mask plate included in the mask plate assembly according to the disclosure and mask plates in the related arts, in a situation where sub-pixels are made to have a same width, a width of openings of both the first and second color mask plates as well as that of grids between the corresponding openings can be doubled, which can effectively prevent deformations at openings and grids therebetween from imposing bad influence on its performance, such that adverse effect on a shape of sub-pixels to be fabricated can be prevented as well.

BRIEF DESCRIPTION OF THE DRAWINGS

[0025] FIG. 1 is a schematic diagram illustrating a structure of a mask plate assembly according to the related arts;

[0026] FIG. 2 is a schematic diagram illustrating a structure of a mask plate assembly according to a first embodiment of the disclosure;

[0027] FIG. 3 is a schematic diagram illustrating respective electroluminescent layers by using the mask plate assembly according to the first embodiment of the disclosure;

[0028] FIG. 4 is a schematic diagram illustrating a structure of another mask plate assembly according to a second embodiment of the disclosure;

[0029] FIG. 5 is a schematic diagram illustrating a structure of yet another mask plate assembly according to a third embodiment of the disclosure; and

[0030] FIG. 6 is a schematic diagram illustrating respective electroluminescent layers by using the mask plate assembly according to the third embodiment of the disclosure.

DETAILED DESCRIPTION

[0031] In order to make the technical solutions and the advantages of the disclosure more apparent, the disclosure will be described hereinafter in a clear and complete manner in conjunction with the drawings and embodiments. Obviously, the following embodiments are merely a part of, rather than all of, the embodiments of the present disclosure. And based on these embodiments, a person skilled in the art may obtain the other embodiments, which also fall within the scope of the present disclosure. Obviously, the following drawings merely relate to some embodiments of the present disclosure, and based on these drawings, a person skilled in the art may obtain the other drawings without any creative effort.

[0032] It should be indicated herein, embodiments of the disclosure are described only with an electroluminescent layer as an example, which cannot be construed as any limitations on the disclosure. Further, the disclosure is not limited only to an Organic Light-Emitting Diode (OLED) display field, but also includes other display fields such as Liquid Crystal Display (LCD). The layers fabricated by using mask plates are not limited only to the electroluminescent layers, but also include other kinds of layers. A shape of openings of the mask plates is not limited only to a rectangular shape. However, the following description relates to openings of the mask plates having a rectangular shape as an example.

[0033] In any one of embodiments of the disclosure, at least one of three mask plates included in the mask plate assembly is a metal mask plate.

First Embodiment

[0034] FIG. 2 is a schematic diagram illustrating a structure of a mask plate assembly according to the first embodiment of the disclosure. As shown in FIG. 2, the mask plate assembly includes a mask plate 100, a mask plate 200, a mask plate 310, and a mask plate 320. Each of the above mask plates includes a non-masking region and a masking region. Here the masking region is referred to as a region corresponding to a display region of a display panel to be fabricated, while the non-masking region is referred to as peripheral regions enclosing the masking region. Specifically, the masking region includes rectangular openings extended in a column direction and grids located between corresponding openings and used for defining rectangular openings. Further, a length of the rectangular openings is substantially the same as that of the masking region in the column direction.

[0035] Rectangular openings A of the mask plate 100 and rectangular openings B of the mask plate 200 are of a width of W (in a row direction), while rectangular openings C1 of the mask plate 310 and rectangular openings C2 of the mask plate 320 are of a width of W/2. As shown in FIG. 2, when the mask plate 100, the mask plate 200, the mask plate 310, and the mask plate 320 are aligned with one another, the

rectangular openings A, B, C1, and C2 are arranged alternately in the row direction in an order of a rectangular opening A, a rectangular opening C1, a rectangular opening B, and a rectangular opening C2. Both the rectangular openings C1 and C2 are arranged respectively at regions between one rectangular opening A and each of two rectangular openings B immediately adjacent to the one rectangular opening A.

[0036] In one possible embodiment, respective electroluminescent layers of an Organic Light-Emitting Diode (OLED) display panel are fabricated by using the mask plate assembly provided by the first embodiment of the disclosure. The method for fabricating respective electroluminescent layers may include: fabricating a first electroluminescent layer pattern for displaying a first color (e.g., red (R)) by using the mask plate **100**; fabricating a second electroluminescent layer pattern for displaying a second color (e.g., blue (B)) by using the mask plate **200**; fabricating a third electroluminescent layer pattern for displaying a third color (e.g., green (G)) by using the mask plate **310** and the mask plate **320**.

[0037] Respective electroluminescent layers fabricated by using the above mask plate assembly are shown in FIG. 3. The respective electroluminescent layers include a first electroluminescent layer pattern for displaying the first color (which consists of electroluminescent material blocks “a” each having a strip-like shape as shown in FIG. 3), a second electroluminescent layer pattern for displaying the second color (which consists of electroluminescent material blocks “b” each having a strip-like shape as shown in FIG. 3), and a third electroluminescent layer pattern for displaying the third color (which consists of electroluminescent material blocks “c” each having a strip-like shape as shown in FIG. 3). Therefore, in the row direction, the electroluminescent material blocks “a”, the electroluminescent material blocks “b”, and the electroluminescent material blocks “c” are arranged alternately in an order of an electroluminescent material block “a”, an electroluminescent material block “c”, an electroluminescent material block “b”, and another electroluminescent material block “c”.

[0038] In one possible embodiment, a process of fabricating electroluminescent layer patterns is referred to as forming organic electroluminescent materials on a corresponding base substrate by an evaporation process using respective mask plates.

[0039] Referring to FIG. 3, in a possible embodiment, each of electroluminescent material blocks may be divided into many rows of sub-pixels. Specifically, each of the electroluminescent material blocks “a” is divided into two columns of first color sub-pixels, each of the electroluminescent material blocks “b” is divided into two columns of second color sub-pixels, and each of the electroluminescent material blocks “c” corresponds to one column of third color sub-pixels. As a result, each of the third color sub-pixels is of a width of $W/2$. In doing so, when display driving is conducted, one third color sub-pixel as well as one first color sub-pixel and one second color sub-pixel adjacent to the one third color sub-pixel are taken as one pixel unit for displaying. By comparison with FIG. 1, in a situation where sub-pixels to be fabricated are of a same width, an opening width and a grid width of the mask plates **100**, **200** included in the mask plate assembly according to embodiments of the disclosure are twice as wide as those of the mask plates **100**, **200** included in the mask plate assembly according to the

related arts. Put it another way, in the mask plates according to the related arts, with respect to the mask plate **100** or **200**, a distance between any two openings, i.e., a width of one grid, is twice as wide as that of one sub-pixel. As opposed to it, according to embodiments of the disclosure, with respect to the mask plate **100** or **200**, a distance between two rectangular openings A or B is four times as wide as that of one sub-pixel. As a result, a grid width of the mask plate **100** or **200** doubles in relation to its original width, which can effectively prevent grids and openings from deforms. In other words, provided that there is a constant thickness, the wider grids and openings are, the more likely strain forces on them decentralize, and the harder for them to experience deforms.

[0040] In one possible embodiment, the method for fabricating an OLED display panel usually further includes a step of patterning a pixel electrode. Respective electroluminescent material blocks are divided into a plurality of sub-pixels by such a step of patterning the pixel electrode, according to this disclosure. Specifically, by choosing suitable fabricating processes, the pixel electrode pattern includes a plurality of pixel electrode blocks. That is to say, the electroluminescent material blocks “a” and the electroluminescent material blocks “b” correspond to N rows and two columns of pixel electrode blocks within the pixel electrode pattern to be fabricated, where N represents a number of sub-pixels within one column of sub-pixels of the OLED display panel to be fabricated. On the other hand, each of the electroluminescent material blocks “c” corresponds to N rows and one column of pixel electrode blocks within the pixel electrode pattern to be fabricated.

[0041] It should be appreciated that the pixel electrode block within the pixel electrode pattern is referred to as an electrode that is connected with a pixel driver and used for display control.

[0042] In the first embodiment of the disclosure, the mask plates **310** and **320** are in combination to form a third color mask plate used for fabricating sub-pixels for displaying the third color. As compared with FIG. 1, in embodiments of the disclosure, in a situation where sub-pixels to be fabricated are of a same width, a grid width of the mask plates **310** and **320** becomes $5/2$ times as wide as its original width, thereby effectively preventing grids and openings from deforms. In the related art, the distance between two adjacent opening, i.e., the width of a grid, is twice as wide as a width of a sub-pixel. Specifically, in embodiments of the disclosure, with respect to the mask plate **310**, there arranged one rectangular opening A, one rectangular opening B, and one rectangular opening C2 between two immediately adjacent rectangular openings C1. As a result, a width of a corresponding grid is five times as wide as that of a sub-pixel. Alike, with respect to the mask plate **320**, a width of a corresponding grid is also five times as wide as that of a sub-pixel. Accordingly, a grid width of the mask plates **310** and **320** becomes $5/2$ times as wide as its original width.

Second Embodiment

[0043] As different from the first embodiment, in the second embodiment, the mask plates **310** and **320** are replaced with one mask plate **300** having a plurality of rectangular openings C as shown in FIG. 4. When the mask plates **100**, **200**, and **300** are aligned with one another, in a row direction, the rectangular openings A, B, and C are arranged alternately in an order of a rectangular opening A,

a rectangular opening C, a rectangular opening B, and another rectangular opening C.

[0044] The method for fabricating respective electroluminescent layers by using the mask plate assembly provided by the second embodiment may refer to that described in the first embodiment. The difference is that only one mask plate 300, other than two mask plates, is used for fabricating sub-pixels for displaying the third color.

[0045] The respective electroluminescent layers fabricated by using the mask plate assembly provided by the second embodiment may refer to FIG. 3, which will not be described in details.

[0046] The mask plate assembly provided by the second embodiment can reduce the number of mask plates to be used, by comparing with that of the first embodiment.

Third Embodiment

[0047] As shown in FIG. 5, in the third embodiment, rectangular openings A of the mask plate 100 and rectangular openings B of the mask plate 200 are of a relatively smaller length, which is different from the first embodiment. When a plurality of rectangular openings A and a plurality of rectangular openings B are aligned in a column direction, then these openings A and B are arranged alternately. On the other hand, when the plurality of rectangular openings A and the plurality of rectangular openings B are aligned in a row direction, these openings A and B are also arranged alternately. Moreover, as in the first embodiment, both rectangular openings A and rectangular openings B are of a width twice as wide as that of rectangular openings C 1 and C2. Further, single rectangular opening A and single rectangular opening B are of a same length in the column direction, which is taken as L.

[0048] By comparing with the mask plates 100 and 200 in the related arts, grids of the mask plates 100 and 200 in the third embodiment are in a form of a lattice-like structure, which can effectively prevent openings and grids of the mask plates 100 and 200 from deforms.

[0049] Respective electroluminescent layers as shown in FIG. 6 are fabricated by using the mask plate assembly according to the third embodiment. Specifically, the electroluminescent material block "a" formed by one rectangular opening A is divided into two rows and two columns of four sub-pixels for displaying a first color. The electroluminescent material block "b" formed by one rectangular opening B is divided into two rows and two columns of four sub-pixels for displaying a second color. And the electroluminescent material block "c" formed by rectangular openings C1 and C2 is divided into N rows and one column (N represents a resolution in the column direction) of sub-pixels for displaying a third color. As a result, one sub-pixel is of a width of W/2 and a length of L/2.

[0050] Accordingly, in the method for fabricating the OLED display device, with respect to the pixel electrode pattern to be fabricated, the electroluminescent material block "a" formed by one rectangular opening A and the electroluminescent material block "b" formed by one rectangular opening B both correspond to two rows and two columns of sub-pixel electrode blocks.

[0051] In one possible embodiment, the sub-pixel array as shown in FIG. 6 may be driven by the display driving method according to the first embodiment. In other words, one pixel unit PU that includes one third color sub-pixel, and one first color sub-pixel and one second color sub-pixel

immediately adjacent to the one third color sub-pixel is driven for displaying. Alternatively, in another possible embodiment, another display driving method may be performed, as shown in FIG. 6. Specifically, according to this method, one pixel unit PU that includes one first color sub-pixel, one second color sub-pixel immediately adjacent to the one first color sub-pixel in the column direction, and one third color sub-pixel immediately adjacent to the one first color sub-pixel or the one second color sub-pixel in the column direction is driven for displaying. The display driving method as shown in FIG. 6 enables display pictures to look more subtle, compared with conventional display driving methods.

[0052] In another aspect of the disclosure, an OLED display panel and a display device including the same are provided. The OLED display panel includes a sub-pixel array having sub-pixels for displaying three different colors. A specific arrangement of the sub-pixel array may refer to FIG. 3 or FIG. 6. Within one row of sub-pixels, these sub-pixels for displaying different colors are arranged alternately in an order of a first color sub-pixel, another first color sub-pixel, a third color sub-pixel, a second color sub-pixel, another second color sub-pixel, and another third color sub-pixel.

[0053] Further, as shown in FIG. 6, in the above sub-pixel array, each sub-pixel column to which respective first color sub-pixels belong and each sub-pixel column to which respective second color sub-pixels belong include only the first color sub-pixels and the second color sub-pixels. In addition, these first color sub-pixels and these second color sub-pixels are arranged alternately in an order of a first color sub-pixel, another first color sub-pixel, a second color sub-pixel, and another second color sub-pixel.

[0054] Specifically, the above display device may be any devices having displaying function, such as a mobile phone, a desktop computer, a TV, a laptop computer, and so forth.

[0055] In addition, it should be appreciated that the above terms "row" and "column" only indicate two directions perpendicular to each other. There is no intention that "row" is only limited to a lateral direction, while "column" is only limited to a longitudinal direction.

[0056] The above are merely example embodiments of the disclosure and shall not be used to limit the scope of the disclosure. It should be noted that, a person skilled in the art may make improvements and modifications without departing from the principle of the disclosure, and these improvements and modifications shall also fall within the scope of the disclosure.

What is claimed is:

1. A mask plate assembly comprising:

- a first mask plate having a plurality of first openings;
- a second mask plate having a plurality of openings; and
- a third mask plate having a plurality of third openings, wherein the first openings and the second openings have a same length in a row direction, which is twice a length of the third openings in the row direction, and wherein when the first mask plate, the second mask plate, and the third mask plate are aligned with one another, the first openings, the second openings, and the third openings, wherein the openings are arranged alternately in the row direction in an order of a first opening, a third opening, a second opening, and another third opening.

2. The mask plate assembly according to claim 1, wherein the first openings and the second openings are of a same length in a column direction, and wherein the first openings and the second openings are aligned with one another in the column direction and arranged alternately.

3. The mask plate assembly according to claim 1, wherein the third mask plate includes a first sub-mask plate having a plurality of the third openings and a second sub-mask plate having a plurality of the third openings, and wherein when the first sub-mask plate and the second sub-mask plate are aligned with one another, the third openings of the first sub-mask plate and the third openings of the second sub-mask plate are arranged alternately in the row direction.

4. The mask plate assembly according to claim 1, wherein the first openings, the second openings, and the third openings all have a rectangular shape, and wherein at least one of the first mask plate, the second mask plate, and the third mask plate is a metal mask plate.

5. A method for fabricating a display panel component by using the mask plate assembly according to claim 1 comprising:

- fabricating a first layer using the first mask plate;
- fabricating a second layer using the second mask plate;
- and
- fabricating a third layer using the third mask plate.

6. The method according to claim 5, wherein the first layer is a first electroluminescent layer for displaying a first color, wherein the second layer is a second electroluminescent layer for displaying a second color, and wherein the third layer is a third electroluminescent layer for displaying a third color.

7. The method according to claim 6, wherein each of the first electroluminescent layer, the second electroluminescent layer, and the third electroluminescent layer includes a plurality of electroluminescent material blocks configured to display respective colors, and wherein each of the plurality of electroluminescent material blocks corresponds to one corresponding opening of a mask plate configured to fabricate a corresponding electroluminescent layer.

8. The method according to claim 7, wherein fabricating respective electroluminescent layer patterns using respective mask plates comprises fabricating respective electroluminescent layer patterns using an evaporation process.

9. A method for fabricating a display panel comprising fabricating respective electroluminescent layers using the method according to claim 5.

10. The method according to claim 9, wherein the display panel is an Organic Light-Emitting Diode (OLED) display panel, wherein the method further comprises fabricating a

pixel electrode that includes a plurality of pixel electrode blocks, wherein each electroluminescent material block included in the first electroluminescent layer and the second electroluminescent layer corresponds to two columns of pixel electrode blocks, and wherein each electroluminescent material block included in the third electroluminescent layer corresponds to one column of pixel electrode blocks.

11. The method according to claim 9, wherein the respective electroluminescent layers are fabricated using the mask plate assembly, wherein the first openings and the second openings have a same length in a column direction, wherein the first openings and the second openings are aligned with one another in the column direction and arranged alternately, and wherein each electroluminescent material block included in the first electroluminescent layer and the second electroluminescent layer corresponds to two rows and two columns of pixel electrode blocks.

12. An Organic Light-Emitting Diode (OLED) display panel comprising a sub-pixel array having three-color sub-pixels, wherein each row of the sub-pixel array includes the three-color sub-pixels arranged alternately in an order of a first color sub-pixel, another first color sub-pixel, a third color sub-pixel, a second color sub-pixel, another second color sub-pixel, and another third color sub-pixel.

13. The display panel according to claim 12, wherein each column of the sub-pixel array corresponding to the first color and the second color comprises the three-color sub-pixels arranged alternately in an order of a first color sub-pixel, another first color sub-pixel, a second color sub-pixel, and another second color sub-pixel.

14. A method for driving the OLED display panel according to claim 13 comprising:

- dividing respective sub-pixels within the sub-pixel array into a plurality of pixel units for display driving.

15. The method according to claim 14, wherein the plurality of pixel units each comprises a first color sub-pixel, a second color sub-pixel adjacent to the first color sub-pixel in a column direction, and a third color sub-pixel adjacent to the second color sub-pixel in a row direction.

16. The method according to claim 14, wherein the plurality of pixel units each comprises a first color sub-pixel, a second color sub-pixel, and a third color sub-pixel arranged between the first color sub-pixel and the second color sub-pixel.

17. A display device comprising the display panel according to claim 12.

18. A display device comprising the display panel according to claim 13.

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专利名称(译)	掩模组和制造有机发光层的方法，显示板和驱动方法		
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摘要(译)

本发明提供一种掩模板组件，一种制造电致发光层的方法，一种显示面板及其驱动方法。掩模板组件包括第一颜色掩模板，第二颜色掩模板和第三颜色掩模板，每个颜色掩模板具有多个矩形开口。第一彩色掩模板的第一开口和第二彩色掩模板的第二开口在行方向上具有长度，其基本上是行方向上第三彩色掩模板的第三开口的两倍。当各个掩模板彼此对准时，第一开口，第二开口和第三开口以第一开口，第三开口，第二开口和另一个第三开口的顺序交替布置。

